

Figure 1

105

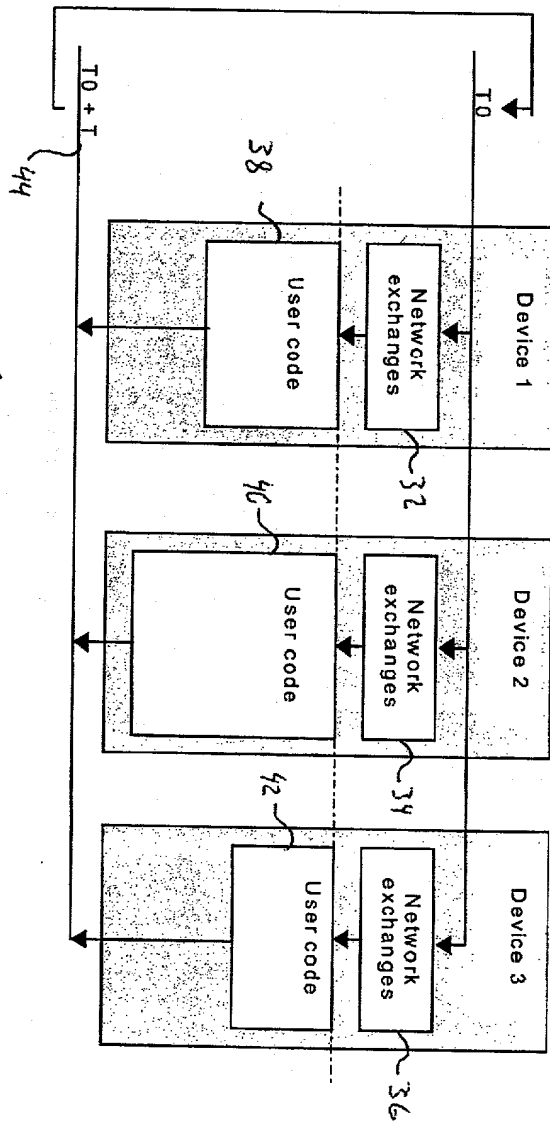
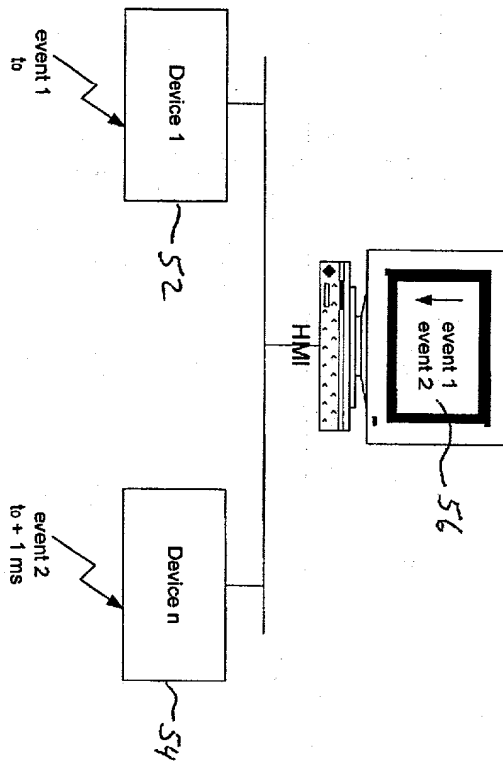


Figure 2

Figure 2 is a block diagram illustrating a system architecture with three devices (Device 1, Device 2, Device 3) connected to a common bus. Each device contains a 'Network exchanges' block and a 'User code' block. Arrows indicate data flow from the bus to the 'Network exchanges' blocks. A dashed line separates the 'Network exchanges' and 'User code' blocks. Labels 32, 34, and 36 point to the 'Network exchanges' blocks of Device 1, Device 2, and Device 3 respectively. Labels 38, 40, and 42 point to the 'User code' blocks of Device 1, Device 2, and Device 3 respectively. A bracket labeled 44 spans the 'User code' blocks of all three devices. Time markers T_0 and $T_0 + T$ are shown at the top of the diagram.

Figure 3



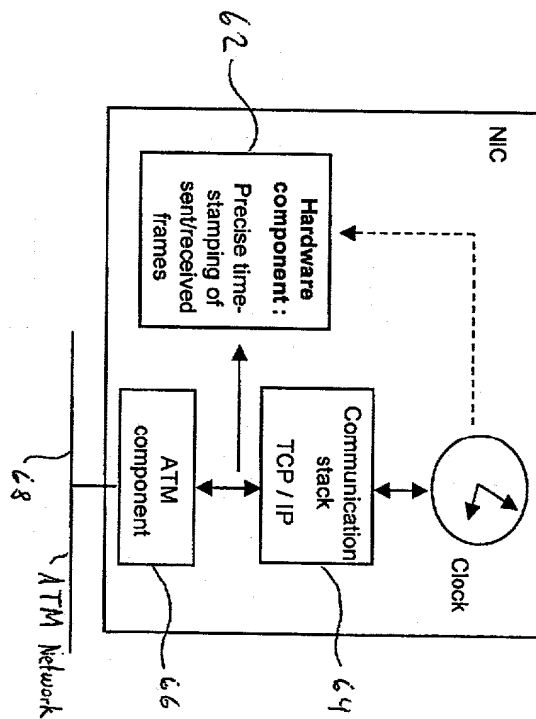


Figure 4

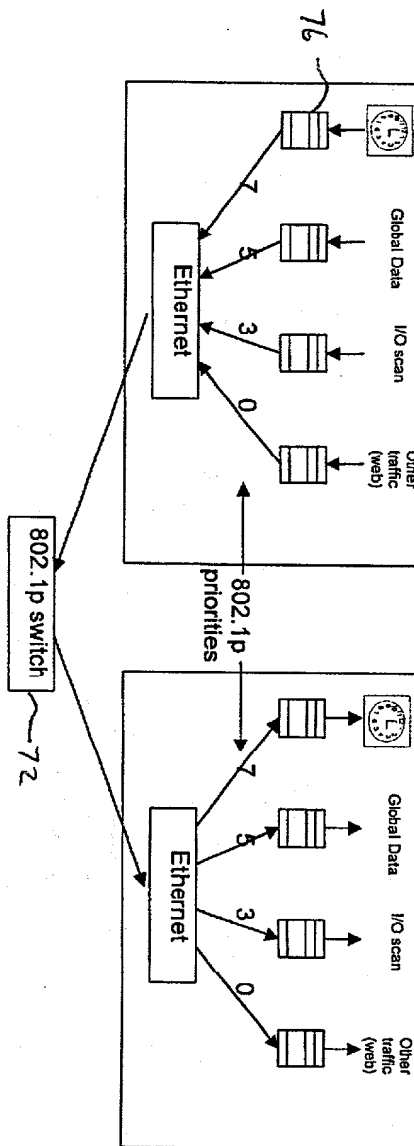


Figure 5

Figure 6

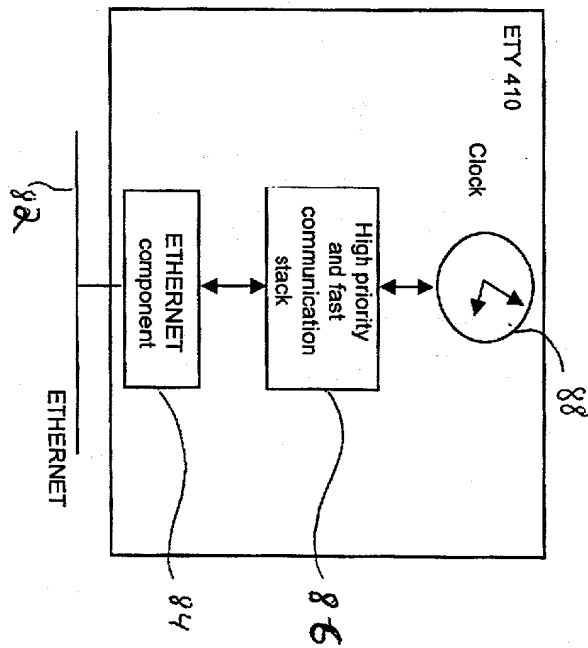
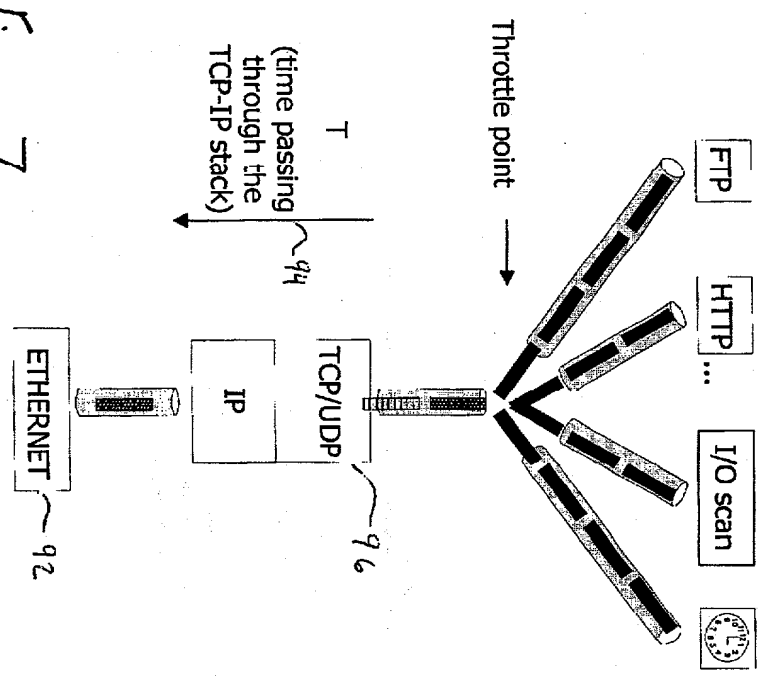


Figure 7



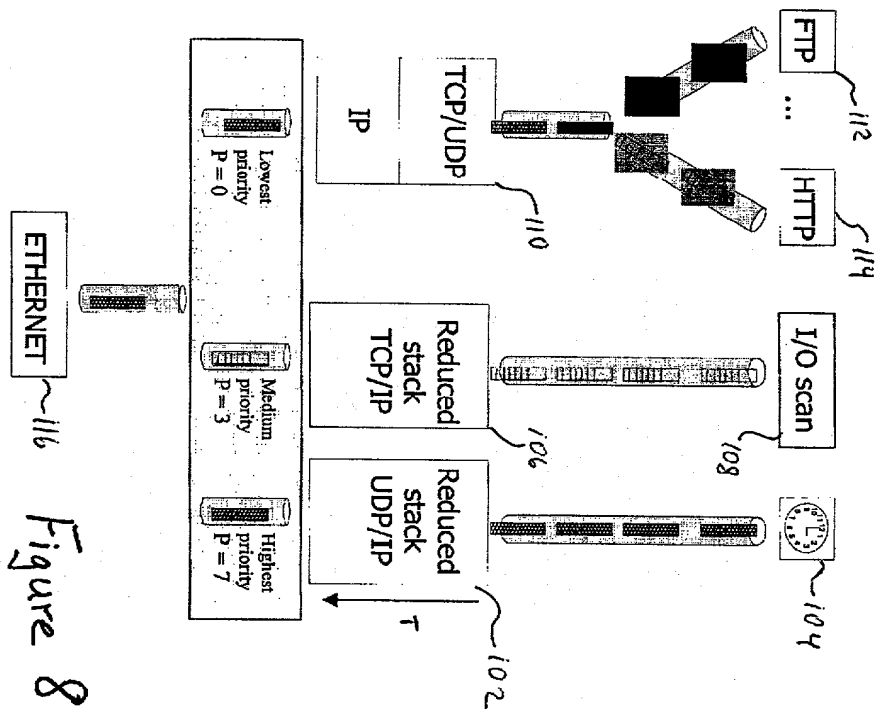


Figure 8

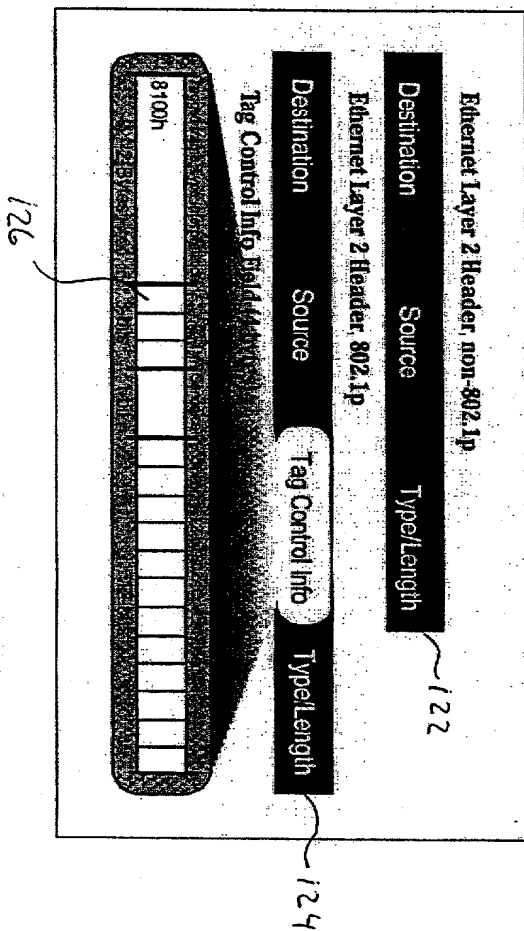


Figure 9

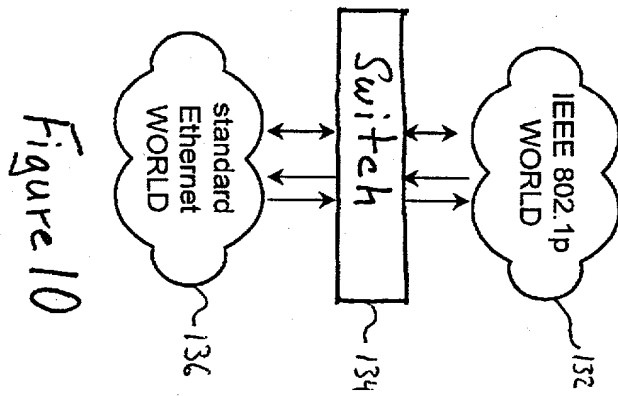


Figure 10

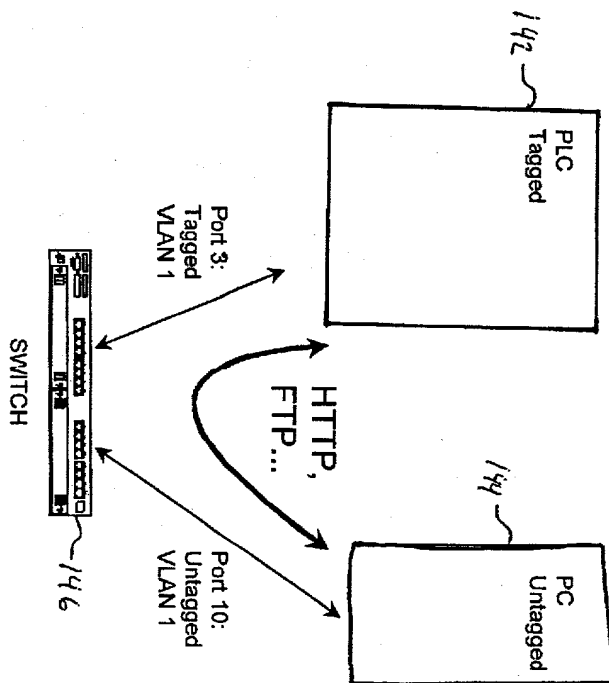


Figure 11

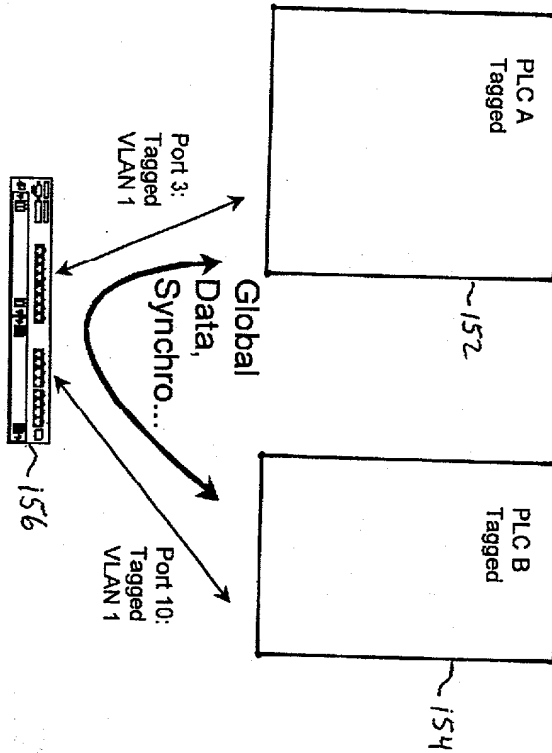
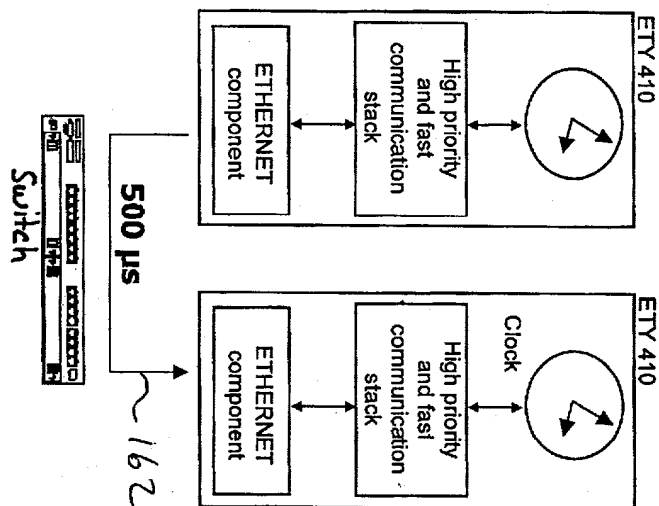
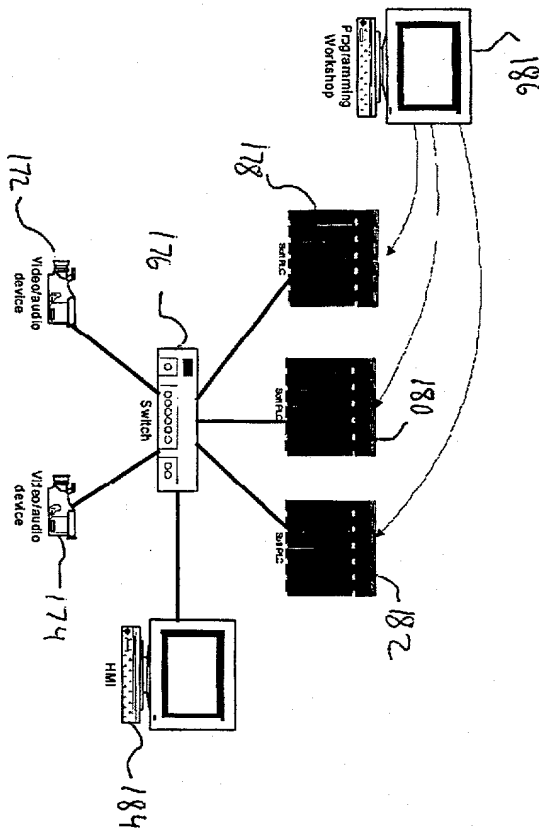


Figure 12

Figure 13





~~AXI~~ → Ethernet 802.1p

Figure 14